

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Term	Documents
(13 AND 9).USPT.	1
(L13 AND L9).USPT.	1

Database:

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EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L14

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Set Name Query

side by side

Hit Count Set Name

result set

*DB=USPT; PLUR=YES; OP=OR*L14 L13 and l91 L14L13 l11 or L12240 L13L12 ((717/163)!.CCLS.)80 L12L11 ((717/159)!.CCLS.)163 L11L10 dictionary near10 extend\$3 near7 entr\$3 and (split\$6 or part\$9)
near5 extend\$31 L10*DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR*L9 dictionary near10 extend\$3 and (split\$6 or part\$9) near5 extend\$356 L9*DB=USPT; PLUR=YES; OP=OR*L8 sql near3 server and defragmentation\$32 L8L7 L6 and sql2 L7L6 L5 and database46 L6L5 L4 and microsoft63 L5L4 L3 and server96 L4L3 defragmentation\$1205 L3L2 defragmentation\$1 and seq4 L2L1 seq near3 server and microsoft2 L1

END OF SEARCH HISTORY

WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 1 through 20 of 47 returned.**☐ 1. Document ID: US 6618833 B1

L17: Entry 1 of 47

File: USPT

Sep 9, 2003

US-PAT-NO: 6618833

DOCUMENT-IDENTIFIER: US 6618833 B1

TITLE: Maintenance of a system model

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC
Draw. Desc	Image										

☐ 2. Document ID: US 6567417 B2

L17: Entry 2 of 47

File: USPT

May 20, 2003

US-PAT-NO: 6567417

DOCUMENT-IDENTIFIER: US 6567417 B2

TITLE: Frame forwarding in a switch fabric

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw. Desc	Image									

☐ 3. Document ID: US 6430188 B1

L17: Entry 3 of 47

File: USPT

Aug. 6, 2002

US-PAT-NO: 6430188

DOCUMENT-IDENTIFIER: US 6430188 B1

TITLE: Unified table for L2, L3, L4, switching and filtering

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMC
Draw. Desc	Image									

☐ 4. Document ID: US 6339820 B1

L17: Entry 4 of 47

File: USPT

Jan 15, 2002

US-PAT-NO: 6339820

DOCUMENT-IDENTIFIER: US 6339820 B1

TITLE: Method and device for carrying out a function assigned to an instruction code

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 5. Document ID: US 6332186 B1

L17: Entry 5 of 47

File: USPT

Dec 18, 2001

US-PAT-NO: 6332186

DOCUMENT-IDENTIFIER: US 6332186 B1

TITLE: Vector register addressing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 6. Document ID: US 6304963 B1

L17: Entry 6 of 47

File: USPT

Oct 16, 2001

US-PAT-NO: 6304963

DOCUMENT-IDENTIFIER: US 6304963 B1

TITLE: Handling exceptions occurring during processing of vector instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 7. Document ID: US 6286130 B1

L17: Entry 7 of 47

File: USPT

Sep 4, 2001

US-PAT-NO: 6286130

DOCUMENT-IDENTIFIER: US 6286130 B1

TITLE: Software implemented method for automatically validating the correctness of parallel computer programs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 8. Document ID: US 6282634 B1

L17: Entry 8 of 47

File: USPT

Aug 28, 2001

US-PAT-NO: 6282634

DOCUMENT-IDENTIFIER: US 6282634 B1

TITLE: Apparatus and method for processing data having a mixed vector/scalar register file

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 9. Document ID: US 6247113 B1

L17: Entry 9 of 47

File: USPT

Jun 12, 2001

US-PAT-NO: 6247113

DOCUMENT-IDENTIFIER: US 6247113 B1

TITLE: Coprocessor opcode division by data type

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 10. Document ID: US 6240508 B1

L17: Entry 10 of 47

File: USPT

May 29, 2001

US-PAT-NO: 6240508

DOCUMENT-IDENTIFIER: US 6240508 B1

TITLE: Decode and execution synchronized pipeline processing using decode generated memory read queue with stop entry to allow execution generated memory read

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 11. Document ID: US 6189094 B1

L17: Entry 11 of 47

File: USPT

Feb 13, 2001

US-PAT-NO: 6189094

DOCUMENT-IDENTIFIER: US 6189094 B1

TITLE: Recirculating register file

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 12. Document ID: US 6170001 B1

L17: Entry 12 of 47

File: USPT

Jan 2, 2001

US-PAT-NO: 6170001

DOCUMENT-IDENTIFIER: US 6170001 B1

TITLE: System for transferring format data from format register to memory wherein format data indicating the distribution of single or double precision data type in the register bank

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 13. Document ID: US 6141742 A

L17: Entry 13 of 47

File: USPT

Oct 31, 2000

US-PAT-NO: 6141742

DOCUMENT-IDENTIFIER: US 6141742 A

TITLE: Method for reducing number of bits used in storage of instruction address pointer values

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 14. Document ID: US 6122275 A

L17: Entry 14 of 47

File: USPT

Sep 19, 2000

US-PAT-NO: 6122275

DOCUMENT-IDENTIFIER: US 6122275 A

TITLE: Real-time processing for virtual circuits in packet switching

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 15. Document ID: US 6075937 A

L17: Entry 15 of 47

File: USPT

Jun 13, 2000

US-PAT-NO: 6075937

DOCUMENT-IDENTIFIER: US 6075937 A

**** See image for Certificate of Correction ****

TITLE: Preprocessing of stored target routines for controlling emulation of incompatible instructions on a target processor and utilizing target processor feedback for controlling non-sequential incompatible instruction emulation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 16. Document ID: US 6009261 A

L17: Entry 16 of 47

File: USPT

Dec 28, 1999

US-PAT-NO: 6009261

DOCUMENT-IDENTIFIER: US 6009261 A

TITLE: Preprocessing of stored target routines for emulating incompatible instructions on a target processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 17. Document ID: US 5946484 A

L17: Entry 17 of 47

File: USPT

Aug 31, 1999

US-PAT-NO: 5946484

DOCUMENT-IDENTIFIER: US 5946484 A

TITLE: Method of recovering source code from object code

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 18. Document ID: US 5923341 A

L17: Entry 18 of 47

File: USPT

Jul 13, 1999

US-PAT-NO: 5923341

DOCUMENT-IDENTIFIER: US 5923341 A

TITLE: Raster operation lookup and execution

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 19. Document ID: US 5881312 A

L17: Entry 19 of 47

File: USPT

Mar 9, 1999

US-PAT-NO: 5881312

DOCUMENT-IDENTIFIER: US 5881312 A

TITLE: Memory transfer apparatus and method useful within a pattern recognition system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 20. Document ID: US 5825921 A

L17: Entry 20 of 47

File: USPT

Oct 20, 1998

US-PAT-NO: 5825921

DOCUMENT-IDENTIFIER: US 5825921 A

TITLE: Memory transfer apparatus and method useful within a pattern recognition system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KIMC

[Generate Collection](#)[Print](#)

Term	Documents
ENTR\$3	0
ENTR	95
ENTRA	12
ENTRAC	1
ENTRACE	224
ENTRACT	5
ENTRADA	33
ENTRAI	1
ENTRAIL	46
ENTRAIN	9655
ENTRAL	39
(L16 AND ENTR\$3).USPT.	47

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Display Format: [TI](#)[Change Format](#)[Previous Page](#)[Next Page](#)

WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 21 through 40 of 47 returned.**☐ 21. Document ID: US 5825373 A

L17: Entry 21 of 47

File: USPT

Oct 20, 1998

US-PAT-NO: 5825373

DOCUMENT-IDENTIFIER: US 5825373 A

TITLE: Raster operation lookup and execution

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KIMC](#)☐ 22. Document ID: US 5819056 A

L17: Entry 22 of 47

File: USPT

Oct 6, 1998

US-PAT-NO: 5819056

DOCUMENT-IDENTIFIER: US 5819056 A

**** See image for Certificate of Correction ****

TITLE: Instruction buffer organization method and system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KIMC](#)☐ 23. Document ID: US 5809320 A

L17: Entry 23 of 47

File: USPT

Sep 15, 1998

US-PAT-NO: 5809320

DOCUMENT-IDENTIFIER: US 5809320 A

TITLE: High-performance multi-processor having floating point unit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KIMC](#)☐ 24. Document ID: US 5809273 A

L17: Entry 24 of 47

File: USPT

Sep 15, 1998

US-PAT-NO: 5809273

DOCUMENT-IDENTIFIER: US 5809273 A

TITLE: Instruction predecode and multiple instruction decode

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 25. Document ID: US 5781758 A

L17: Entry 25 of 47

File: USPT

Jul 14, 1998

US-PAT-NO: 5781758

DOCUMENT-IDENTIFIER: US 5781758 A

TITLE: Software emulation system with reduced memory requirements

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 26. Document ID: US 5781750 A

L17: Entry 26 of 47

File: USPT

Jul 14, 1998

US-PAT-NO: 5781750

DOCUMENT-IDENTIFIER: US 5781750 A

TITLE: Dual-instruction-set architecture CPU with hidden software emulation mode

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 27. Document ID: US 5717908 A

L17: Entry 27 of 47

File: USPT

Feb 10, 1998

US-PAT-NO: 5717908

DOCUMENT-IDENTIFIER: US 5717908 A

**** See image for Certificate of Correction ****

TITLE: Pattern recognition system using a four address arithmetic logic unit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 28. Document ID: US 5652889 A

L17: Entry 28 of 47

File: USPT

Jul 29, 1997

US-PAT-NO: 5652889

DOCUMENT-IDENTIFIER: US 5652889 A

TITLE: Alternate execution and interpretation of computer program having code at unknown locations due to transfer instructions having computed destination addresses

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 29. Document ID: US 5649203 A

L17: Entry 29 of 47

File: USPT

Jul 15, 1997

US-PAT-NO: 5649203

DOCUMENT-IDENTIFIER: US 5649203 A

TITLE: Translating, executing, and re-translating a computer program for finding and translating program code at unknown program addresses

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 30. Document ID: US 5560039 A

L17: Entry 30 of 47

File: USPT

Sep 24, 1996

US-PAT-NO: 5560039

DOCUMENT-IDENTIFIER: US 5560039 A

**** See image for Certificate of Correction ****

TITLE: Apparatus and method for a four address arithmetic unit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 31. Document ID: US 5542058 A

L17: Entry 31 of 47

File: USPT

Jul 30, 1996

US-PAT-NO: 5542058

DOCUMENT-IDENTIFIER: US 5542058 A

TITLE: Pipelined computer with operand context queue to simplify context-dependent execution flow

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 32. Document ID: US 5507030 A

L17: Entry 32 of 47

File: USPT

Apr 9, 1996

US-PAT-NO: 5507030

DOCUMENT-IDENTIFIER: US 5507030 A

TITLE: Successive translation, execution and interpretation of computer program having code at unknown locations due to execution transfer instructions having

computed destination addresses

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 33. Document ID: US 5488730 A

L17: Entry 33 of 47

File: USPT

Jan 30, 1996

US-PAT-NO: 5488730

DOCUMENT-IDENTIFIER: US 5488730 A

TITLE: Register conflict scoreboard in pipelined computer using pipelined reference counts

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 34. Document ID: US 5481689 A

L17: Entry 34 of 47

File: USPT

Jan 2, 1996

US-PAT-NO: 5481689

DOCUMENT-IDENTIFIER: US 5481689 A

TITLE: Conversion of internal processor register commands to I/O space addresses

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 35. Document ID: US 5471591 A

L17: Entry 35 of 47

File: USPT

Nov 28, 1995

US-PAT-NO: 5471591

DOCUMENT-IDENTIFIER: US 5471591 A

TITLE: Combined write-operand queue and read-after-write dependency scoreboard

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 36. Document ID: US 5459798 A

L17: Entry 36 of 47

File: USPT

Oct 17, 1995

US-PAT-NO: 5459798

DOCUMENT-IDENTIFIER: US 5459798 A

**** See image for Certificate of Correction ****

TITLE: System and method of pattern recognition employing a multiprocessing pipelined apparatus with private pattern memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 37. Document ID: US 5450555 A

L17: Entry 37 of 47

File: USPT

Sep 12, 1995

US-PAT-NO: 5450555

DOCUMENT-IDENTIFIER: US 5450555 A

TITLE: Register logging in pipelined computer using register log queue of register content changes and base queue of register log queue pointers for respective instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 38. Document ID: US 5428786 A

L17: Entry 38 of 47

File: USPT

Jun 27, 1995

US-PAT-NO: 5428786

DOCUMENT-IDENTIFIER: US 5428786 A

TITLE: Branch resolution via backward symbolic execution

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 39. Document ID: US 5394529 A

L17: Entry 39 of 47

File: USPT

Feb 28, 1995

US-PAT-NO: 5394529

DOCUMENT-IDENTIFIER: US 5394529 A

TITLE: Branch prediction unit for high-performance processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMC

☐ 40. Document ID: US 5333296 A

L17: Entry 40 of 47

File: USPT

Jul 26, 1994

US-PAT-NO: 5333296

DOCUMENT-IDENTIFIER: US 5333296 A

TITLE: Combined queue for invalidates and return data in multiprocessor system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/M/C

[Generate Collection](#)[Print](#)

Term	Documents
ENTR\$3	0
ENTR	95
ENTRA	12
ENTRAC	1
ENTRACE	224
ENTRACT	5
ENTRADA	33
ENTRAI	1
ENTRAIL	46
ENTRAIN	9655
ENTRAL	39
(L16 AND ENTR\$3).USPT.	47

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Term	Documents
(13 AND 17).USPT.	1
(L17 AND L13).USPT.	1

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Search:

L18

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Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=OR

<u>L18</u>	L17 and l13	1	<u>L18</u>
<u>L17</u>	L16 and entr\$3	47	<u>L17</u>
<u>L16</u>	(table or dictionary) near9 (opcode\$3) near9 (pars\$3 or part\$9 or extend\$3)	51	<u>L16</u>
<u>L15</u>	(table or dictionary) near9 (opcode\$3 or code1) near9 (pars\$3 or part\$9 or extend\$3)	51	<u>L15</u>
<u>L14</u>	L13 and l9	1	<u>L14</u>
<u>L13</u>	l11 or L12	240	<u>L13</u>
<u>L12</u>	((717/163)!.CCLS.)	80	<u>L12</u>
<u>L11</u>	((717/159)!.CCLS.)	163	<u>L11</u>
<u>L10</u>	dictionary near10 extend\$3 near7 entr\$3 and (split\$6 or part\$9) near5 extend\$3	1	<u>L10</u>

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L9</u>	dictionary near10 extend\$3 and (split\$6 or part\$9) near5 extend\$3	56	<u>L9</u>
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DB=USPT; PLUR=YES; OP=OR

<u>L8</u>	sql near3 server and defragmentation\$3	2	<u>L8</u>
<u>L7</u>	L6 and sql	2	<u>L7</u>
<u>L6</u>	L5 and database	46	<u>L6</u>
<u>L5</u>	L4 and microsoft	63	<u>L5</u>
<u>L4</u>	L3 and server	96	<u>L4</u>
<u>L3</u>	defragmentation\$1	205	<u>L3</u>
<u>L2</u>	defragmentation\$1 and seq	4	<u>L2</u>
<u>L1</u>	seq near3 server and microsoft	2	<u>L1</u>

END OF SEARCH HISTORY

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Term	Documents
(13 AND 17).USPT.	1
(L17 AND L13).USPT.	1

Database:

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Search:

L18

[Refine Search](#)[Recall Text](#)[Clear](#)**Search History****DATE:** Tuesday, October 28, 2003 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=OR

<u>L18</u>	L17 and l13	1	<u>L18</u>
<u>L17</u>	L16 and entr\$3	47	<u>L17</u>
<u>L16</u>	(table or dictionary) near9 (opcode\$3) near9 (pars\$3 or part\$9 or extend\$3)	51	<u>L16</u>
<u>L15</u>	(table or dictionary) near9 (opcode\$3 or code1) near9 (pars\$3 or part\$9 or extend\$3)	51	<u>L15</u>
<u>L14</u>	L13 and l9	1	<u>L14</u>
<u>L13</u>	l11 or L12	240	<u>L13</u>
<u>L12</u>	((717/163)!.CCLS.)	80	<u>L12</u>
<u>L11</u>	((717/159)!.CCLS.)	163	<u>L11</u>
<u>L10</u>	dictionary near10 extend\$3 near7 entr\$3 and (split\$6 or part\$9) near5 extend\$3	1	<u>L10</u>

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L9</u>	dictionary near10 extend\$3 and (split\$6 or part\$9) near5 extend\$3	56	<u>L9</u>
-----------	---	----	-----------

DB=USPT; PLUR=YES; OP=OR

<u>L8</u>	sql near3 server and defragmentation\$3	2	<u>L8</u>
<u>L7</u>	L6 and sql	2	<u>L7</u>
<u>L6</u>	L5 and database	46	<u>L6</u>
<u>L5</u>	L4 and microsoft	63	<u>L5</u>
<u>L4</u>	L3 and server	96	<u>L4</u>
<u>L3</u>	defragmentation\$1	205	<u>L3</u>
<u>L2</u>	defragmentation\$1 and seq	4	<u>L2</u>
<u>L1</u>	seq near3 server and microsoft	2	<u>L1</u>

END OF SEARCH HISTORY

WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 1 through 10 of 10 returned.**☐ 1. Document ID: US 6463582 B1

L21: Entry 1 of 10

File: USPT

Oct 8, 2002

US-PAT-NO: 6463582

DOCUMENT-IDENTIFIER: US 6463582 B1

TITLE: Dynamic optimizing object code translator for architecture emulation and dynamic optimizing object code translation method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	K/M/C
Draw Desc	Image										

☐ 2. Document ID: US 6327699 B1

L21: Entry 2 of 10

File: USPT

Dec 4, 2001

US-PAT-NO: 6327699

DOCUMENT-IDENTIFIER: US 6327699 B1

TITLE: Whole program path profiling

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	K/M/C
Draw Desc	Image										

☐ 3. Document ID: US 6247174 B1

L21: Entry 3 of 10

File: USPT

Jun 12, 2001

US-PAT-NO: 6247174

DOCUMENT-IDENTIFIER: US 6247174 B1

TITLE: Optimization of source code with embedded machine instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	K/M/C
Draw Desc	Image									

☐ 4. Document ID: US 6226789 B1

L21: Entry 4 of 10

File: USPT

May 1, 2001

US-PAT-NO: 6226789

DOCUMENT-IDENTIFIER: US 6226789 B1

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for data flow analysis

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 5. Document ID: US 6091897 A

L21: Entry 5 of 10

File: USPT

Jul 18, 2000

US-PAT-NO: 6091897

DOCUMENT-IDENTIFIER: US 6091897 A

**** See image for Certificate of Correction ****

TITLE: Fast translation and execution of a computer program on a non-native architecture by use of background translator

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 6. Document ID: US 5930509 A

L21: Entry 6 of 10

File: USPT

Jul 27, 1999

US-PAT-NO: 5930509

DOCUMENT-IDENTIFIER: US 5930509 A

TITLE: Method and apparatus for performing binary translation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 7. Document ID: US 5842017 A

L21: Entry 7 of 10

File: USPT

Nov 24, 1998

US-PAT-NO: 5842017

DOCUMENT-IDENTIFIER: US 5842017 A

TITLE: Method and apparatus for forming a translation unit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 8. Document ID: US 5613117 A

L21: Entry 8 of 10

File: USPT

Mar 18, 1997

US-PAT-NO: 5613117

DOCUMENT-IDENTIFIER: US 5613117 A

TITLE: Optimizing compiler using templates corresponding to portions of an

- intermediate language graph to determine an order of evaluation and to allocate lifetimes to temporary names for variables

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
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☐ 9. Document ID: US 5488714 A

L21: Entry 9 of 10

File: USPT

Jan 30, 1996

US-PAT-NO: 5488714

DOCUMENT-IDENTIFIER: US 5488714 A

TITLE: Computer program analyzer for adapting computer programs to different architectures

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 10. Document ID: US 4667290 A

L21: Entry 10 of 10

File: USPT

May 19, 1987

US-PAT-NO: 4667290

DOCUMENT-IDENTIFIER: US 4667290 A

TITLE: Compilers using a universal intermediate language

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

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